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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OHASHI, Hitoshi et al.**

Group Art Unit: 1792

Serial No.: 10/047,992

Examiner: **Brian K. Talbot**

Filed: **January 17, 2002**

P.T.O. Confirmation No.: 5363

FOR: **METHOD AND APPARATUS FOR FORMING ELECTRIC CIRCUIT FIXING THE SEMICONDUCTOR DEVICE USING SUBSTRATE JIG**

Ok to enter  
BKT 4/23/08

**RESPONSE UNDER 37 CFR §1.116**  
**- EXPEDITED RESPONSE -**  
**GROUP ART UNIT 1792**

**MAILSTOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

April 8, 2008

Sir:

In response to the Office Action dated **January 8, 2008**, entry of the following amendments and remarks, reconsideration and further examination are respectfully requested.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 8 of this paper.